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Applications of Digital Signal Processing

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eas.caltech.edu/news/1174. Prof. P. P. Vaidyanathan has been with the Electrical Engineering department of the California Institute of Technology since 1983. His main research interests are in digital signal processing,

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sparse array signal processing, spectrum sensing and applications, multirate systems and filter banks, wavelet transforms, compressive sensing and sparse reconstruction, genomic signal processing, and signal processing for digital communications.

Caltech DSP Group: Professor P. P. Vaidyanathan
Palghat P. Vaidyanathan is the Kiyoo and Eiko Tomiyasu Professor of Electrical Engineering at the California Institute of Technology, Pasadena, California, USA, where he teaches and leads research in the area of signal processing, especially digital signal processing, and its applications. He has authored four books, and

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authored or coauthored close to five hundred papers in various IEEE journals and conferences. Prof. Vaidyanathan received his B.Tech and M. Tech degrees from the Institute of Rad

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microphone speech data segmentation and recognition using neural networks, 12th IEEE Digital Signal Processing Workshop/4th IEEE Signal Processing Education Workshop, Publisher: IEEE, Pages: 262-267
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signal processing the sampling rate of a signal is changed in order to increase the efficiency of various signal processing operations. Decimation, or down-sampling, reduces the sampling rate, whereas expansion, or up-sampling, followed by interpolation increases the sampling rate.

Multirate digital signal processing - Åbo Akademi
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areas of signal processing, such as filter bank theory

Multirate Signal Processing Concepts in Digital Communications

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