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Navigator is a collection of hardware components that facilitate data movement and multi-core control. • The major components within the Navigator domain are: – A hardware Queue

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My knowledge
about Multicore

Navigator is
mostly limited

with the

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Dummies and user
guide documents.

So I have a couple of questions: 1) Is it possible to use Multicore Navigator for a complete infrastructure communication in a complete asymmetric system? If yes,

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I understand
that we need to
use QMSS TX
queues and ...

~~[Resolved]~~

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1. how can i
restrict into 2
core. 2. what are
all the
configuration
changes in that
example code ?

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Since the Packet DMA uses the queue manager, it is assumed that you understand the concepts presented in Part 2 of the Multicore Navigator training. First we will discuss how the Packet

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DMA works. We'll talk about triggering, transmit processing, receive processing, and infrastructure processing.

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PKTDMA

Controller SA PA

GbE Switch

Subsystem INTD

Packet Streaming

Switch

Configuration

Bus SGMII0

SGMII1 PHY stat_

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pend_raw[1:0]
misc_int
buf_starve_intr
mdio_link_intr[1
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packet dma

(pktdma) ... And

you can see all

the specific

fields by

looking at these

registers in the

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lastly, for the

receive DMA, the

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last register to be configured are the channel control registers.

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